

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroyuki Yoshida

Serial No.: not yet assigned

Filed: February 15, 2005

For: "INSULATED GATE FIELD-EFFECT
TRANSISTOR AND ITS
MANUFACTURING METHOD, AND
IMAGING DEVICE AND ITS
MANUFACTURING METHOD"

Case No.: 09792909-6113

Group Art Unit: not yet assigned

Examiner: not yet assigned

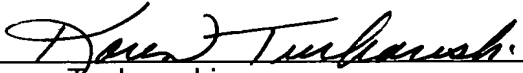
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Karen Turkowski

PRELIMINARY AMENDMENT ACCOMPANYING APPLICATION

Mail Stop: PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Please amend the accompanying application as follows:

Amendments to the Specification begin on page 2 of this document.